

L Number	Hits	Search Text	DB	Time stamp
1	2	("20030046486").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 17:22
14	859	((711/103).CCLS.) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:23
15	942	((713/2).CCLS.) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:26
16	17	(((((713/2).CCLS.) and @ad < "20010830") and flash) and serial) and SDRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:23
17	3	(system adj reset) with (cpu near4 activa\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:23
18	15	((CPU processor system) adj reset) near signal) with (boot)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:24
19	370	((713/2).CCLS.) and @ad < "20010830") and reset and boot	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:26
20	0	((boot near5 load) near (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/31 19:26
-	1057	(711/103).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 18:17
-	600	((711/103).CCLS.) and @ad < "20010830") and flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:57
-	50	((711/103).CCLS.) and @ad < "20010830") and flash) and compression	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 11:57
-	16	((711/103).CCLS.) and @ad < "20010830") and flash) and compression) and decompress\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:10
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (synchronous near2 RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:12
-	3	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (synchronous with RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:12

-	4	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and ((synchronous with RAM) sdram sldram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:13
-	97	(flash with memory with compress\$4) and ((power adj up) boot) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 16:59
-	75	(flash with memory with decompress\$4) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:12
-	2	"20030046483"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:13
-	2	"20030046486"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:14
-	2	"6549468"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:15
-	7	"6560161"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:20
-	2	"20030023840"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/30 17:20
-	2209	(flash with memory) and (power adj up) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 13:38
-	36	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:30
-	34	((flash with memory) with (RAM) with (load read)) same ((power adj up) reset)) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 15:46
-	184	((flash with memory) with (RAM) with (load\$3 read\$3 stor\$3 cop\$4)) same ((power adj up) reset\$4 boot\$4)) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 15:47
-	152	((flash near4 memory) with (RAM) with (load\$3 read\$3 stor\$3 cop\$4)) same ((power adj up) reset\$4 boot\$4)) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 16:13
-	2	"20020138702"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/03 16:18

-	2	"20020016888"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 18:22
-	2	"20020016888" and (exclusive adj lock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/04 18:23
-	2	"20030046485"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:16
-	338	((CPU processor system) adj reset) with (boot)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:22
-	232	((CPU processor system) adj reset) with (boot)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/12 21:18
-	3	((CPU processor system) adj reset) with (boot near5 (preload (pre adj load))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:33
-	15	((boot near5 load) near (complet\$4 ack acknowledg\$5)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:41
-	9	((boot near5 load) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 13:43
-	2	((boot near5 load\$4) near4 (complet\$4 after)) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:27
-	65	(flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:28
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and RDRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:28
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (Rambus with DRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:29
-	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (Rambus with RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:29
-	2566	(Rambus with (RAM dram rdram))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:31

-	4	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)and rambus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:30
-	1504	((Rambus with (RAM dram rdram))) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:33
-	1	((711/103).CCLS.) and @ad < "20010830") and rdram	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:34
-	3	((711/103).CCLS.) and @ad < "20010830") and rambus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/13 18:34
-	0	((flash near3 eprom) with serial) and RDRAM and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 14:05
-	4	((flash near3 eprom) with serial) and @ad < "20010830") and (flash with boot)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 14:06
-	112	((flash near3 eprom) with serial) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 16:15
-	0	((flash near3 (serial adj bus)) with boot) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 14:37
-	1	((flash near3 (serial)) with boot) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 14:38
-	0	((flash near5 (serial adj bus)) same boot) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 14:38
-	29	((flash near5 (serial adj bus))) and @ad < "20010830"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 14:38
-	1008	(713/2).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 16:16
-	236074	((713/2).CCLS.) and @ad < "20010830") flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 16:16
-	369	((713/2).CCLS.) and @ad < "20010830") and flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/14 16:16

-	164	(((713/2).CCLS.) and @ad < "20010830") and flash) and serial	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/05/14 16:16
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